

09/300,845

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,768,546 B2  
DATED : July 27, 2004  
INVENTOR(S) : Hiroshi Sato

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [\*] Notice, insert -- This patent issued on a continued prosecution application filed under 37 CFR 1.53(d), and is subject to the twenty year patent term provisions of 35 U.S.C. 154(a)(2). --

Column 3,

Line 37, "step and-scan" should read -- step-and-scan --.

Column 4,

Line 62, "wafer 1B," should read -- wafer 18, --.

Column 5,

Line 65, "on axis" should read -- on-axis --.

Column 9,

Line 38, " $I_0 \times D_0 = I_1 \times D_1$ ." should read --  $I_0 \times D_0 = I_1 \times D_1$ . --.

Line 59, "CCDS," should read -- CCDs, --.

Line 65, "so prepared" should read -- so-prepared --.

Column 12,

Line 61, "llumination" should read -- illumination --.

Line 64, "iluminance" should read -- illuminance --.

Column 13,

Lines 3, 42 and 62, "illumination" (second occurrence) should read -- illuminating --.

Line 29, "reticla," should read -- reticle, --.

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Column 14,

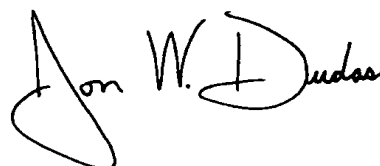
Line 33, "provide," should read -- provided --.

Line 67, "illuminance of" should read -- illuminance --.

Signed and Sealed this

Tenth Day of May, 2005

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A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*